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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent	7,068,672	Grant Date	June 27, 2007
First Inventor	James W. Jones	Filing Date	June 4, 2001
Appl. No.	09/874,395	Confirmation No.	4480
Title:	ASYNCHRONOUS RECEIVE AND TRANSMIT PATCH CROSSPOINT		
Docket No.:	CLX021 US	Customer No.:	34036

Saratoga, California
December 5, 2007

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

Errors appear in the above-identified patent and need to be corrected as shown in the attached proposed "CERTIFICATE OF CORRECTION." Specifically, on the front page of the printed patent under the heading "References Cited, U.S. PATENT DOCUMENTS", there are twenty-five (25) references missing for which the Examiner initialed as being considered during the prosecution of the application, in addition to OTHER PUBLICATIONS, of which there are an additional twenty-seven (27) references missing for which the Examiner initialed as being considered during the prosecution of the application. See the attached copy of the Information Disclosure Statement by Applicant which was initialed by the Examiner. Hence a certificate of correction is hereby requested, pursuant to 35 USC § 254, 37 CFR 1.322 and MPEP 1480.

These errors appear to be made by the USPTO and hence no fee is believed to be needed. If any fee is deemed to be necessary, please charge the fee to the Deposit Account 50-2263 while citing to the Attorney Docket No. CLX021 US. Should there be any questions concerning this submission, please call the undersigned at (408) 378-7777, ext. 113

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I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

S. Omkar

Attorney for Applicant(s)

Dec 5, 2007

Date of Signature

Respectfully submitted,

Certificate

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of Correction

S. Omkar

Omkar K. Suryadevara
Attorney for Applicants and Assignee
Reg. No. 36,320

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 4

PATENT NO.: 7,068,672 B1
APPLICATION NO.: 09/874,395
ISSUE DATE: June 27, 2007
INVENTOR(S): James W. Jones

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

(56) References Cited

Please insert the following U.S. PATENT DOCUMENTS:

5,119,367	06-1992	Kawakatsu et al.
5,295,135	03-1994	Kammerl, Anton
5,365,521	11-1994	Ohnishi et al.
5,455,826	10-1995	Ozveren et al.
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5,577,035	11-1996	Hayter et al.
5,604,867	02-1997	Harwood, Michael J.
5,710,549	10-1998	Horst et al.
5,781,320	07-1998	Byers, Charles Calvin
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5,982,771	11/09/1999	Caldara et al.
5,987,026	11-1999	Holland, Peter
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6,501,731	12-2002	Chong et al.
6,798,784	09-2004	Dove et al.

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Silicon Valley Patent Group LLP
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PATENT NO. 7,246,043 B2

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PCT/US02/17515 PCT Search Report, dated 12 Dec. 2002, 1 pg;

PCT/US02/17515 Int'l Preliminary Examination Report, dated 12 Nov. 2003, 7 pgs;

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N. KcKeown, "Scheduling Algorithms for Input-Queued Cell Switches", © 1995, pp. 1-119;

R. Schoenen, "An Architecture Supporting Quality-of-Service in Virtual-Output-Queued Switches", iEICE Transaction Communications, Vol. E83-B, No. 2, February 2000, pp. 1-10

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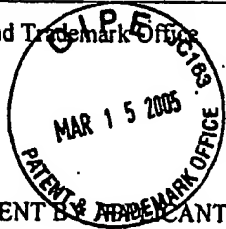
0

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Sheet 1 of 3

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

Application No.:	09/874,402
Filing Date:	June 4, 2001
First Named Inventor:	Jason Dove
Group Art Unit:	2662
Examiner Name:	Unknown
Confirmation No.:	4498
Attorney Docket No.:	CLX023 US

U.S. Patent Documents

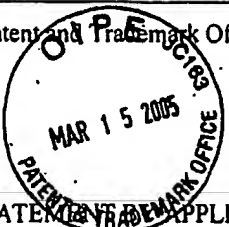
*Examiner Initials		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>HM</i>	1.	6,195,355	02/2001	Demizu	370	397	
<i>HM</i>	2.	6,181,694	01/2001	Pickett, Scott K.	370	458	
<i>HM</i>	3.	5,987,026	11/1999	Holland, Peter	370	353	
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<i>HM</i>	6.	5,781,320	07/1998	Byers, Charles Calvin	370	395.51	
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<i>HM</i>	8.	6,262,986	Jul-01	Oba et al.	370	399	
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<i>HM</i>	10.	6,014,367	Jan-00	Joffe	370	230	
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<i>HM</i>	12.	5,577,035	Nov-96	Hayter et al.	370	60	
<i>HM</i>	13.	5,500,858	Mar-96	McKeown	370	60	
<i>HM</i>	14.	5,923,644	Jul-99	McKeown et al.	370	230	
<i>HM</i>	15.	6,160,812	Dec-00	Bauman et al.	370	416	
<i>HM</i>	16.	6,327,253	Dec-01	Frink	370	260	
<i>HM</i>	17.	6,134,217	Oct-00	Stiliadis et al.	370	232	
<i>HM</i>	18.	5,982,771	9 Nov. 1999	Caldara et al.	370	389	
<i>HM</i>	19.	6,501,731	Dec-02	Chong et al.	370	230.1	
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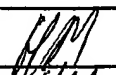
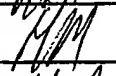
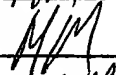
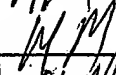
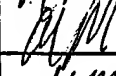
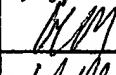
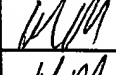
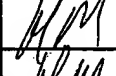


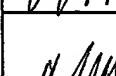
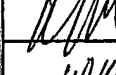
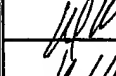
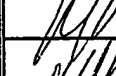
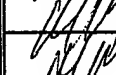
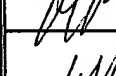
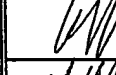
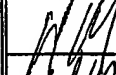
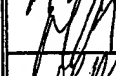
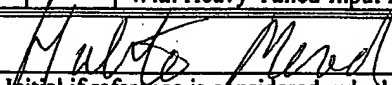
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Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)		
	26.	PCT/US02/17515 PCT Search Report, dated 12 Dec. 2002, 1 page
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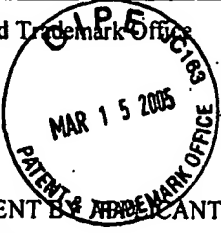
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<i>HM</i>	15.	6,160,812	Dec-00	Bauman et al.	370	416	
<i>HM</i>	16.	6,327,253	Dec-01	Frink	370	260	
<i>HM</i>	17.	6,134,217	Oct-00	Stiliadis et al.	370	232	
<i>HM</i>	18.	5,982,771	9 Nov. 1999	Caldara et al.	370	389	
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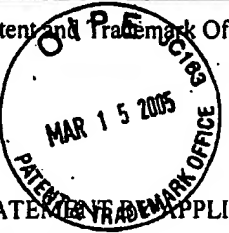
Examiner:

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U.S. Department of Commerce, Patent and Trademark Office  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Application No.:	09/874,402
	Filing Date:	June 4, 2001
	First Named Inventor:	Jason Dove
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Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)		
26.	PCT/US02/17515 PCT Search Report, dated 12 Dec. 2002, 1 page	
27.	PCT/US02/17515 Int'l Preliminary Examination Report, dated 12 Nov. 2003, 7 pages	
28.	Office Action dated September 28, 2004, (US Patent Application 10/199,996) (8 pages)	
29.	Response to Amendment dated September 28, 2004, (US Patent Application 10/199,996), filed January 24, 2005, (8 pages)	
30.	Office Action dated September 28, 2004 in EP Application No. 03254534.5-2416 based on US Patent Application 10/199,996 (5 pages total excluding cover sheet)	
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